

Amendments to the Specification:

Please amend the paragraph beginning at page 10 in line 3 and ending at page 11 in line 23 to read as follows:

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(Currently Amended) The flat panel display device of the present invention illustrated in FIG. 4 includes a flat panel module 15 for receiving image control signals applied to column and row control lines and displaying an image through a plurality of LCD pixels based on the received image control signals, column drivers 22 each adapted to output an image control signal to an associated one of the column control lines, thereby driving associated ones of the LCD pixels included in the flat panel module 15, and row drivers 30 each adapted to output an image control signal to an associated one of the row control lines, thereby driving associated ones of the LCD pixels. The flat panel display device also includes a timing controller 42 for controlling the outputting of the image control signals from the column drivers 22 and row drivers 32 adapted to control the flat panel module 15, a buffer 52 for storing a reference voltage for a gray-scale brightness reference, and a first connector 150 electrically connected to the timing controller 42 and adapted to externally receive an image signal and to output the received image signal to the timing controller 42. The flat panel display device further includes a system board 200 electrically connected to the first connector and adapted to output video control signals to the flat panel module in accordance with a function allocated thereto. This system board 200 includes a second connector 155 electrically connected to the first connector 150 and adapted to

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output a processed image signal for allowing the flat panel module 15 to operate in accordance with a function allocated thereto based on the image control signal, a third connector 158 adapted to externally receive an analog image signal, an analog/digital (A/D) converter 180 for converting the analog image signal received from the third connector ~~150~~158 into a digital image signal, a video converter 182 for receiving a video signal, externally applied thereto, in a matched state, a digital converter 184 for receiving a digital image signal, externally applied thereto, in a matched state, an image processor 160 for performing an image processing for a signal received from the digital converter 184, a low voltage differential signaling (LVDS) unit 170 for converting a signal outputted from the image processor 160 into a low voltage signal to reduce electromagnetic waves, and a main circuit unit (MCU) 190 for controlling and monitoring the functional units of the system board 200. The flat panel display device also includes a chassis 82 adapted to mechanically fix and clamp all components of the flat panel display device and provided at one side wall thereof with an insertion slot for allowing the system board 200 to be separably mounted in the chassis 82.

Please amend the paragraph beginning at page 12 at line 2 and ending at page 12 at line 11 to read as follows:

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(Currently Amended) In the flat panel display device, the timing controller 42 receives the reference voltage from the buffer ~~50~~52 corresponding to the gray-scale brightness reference for the pixels of the LCD constituting the flat panel module 15. Based on the reference voltage, the timing controller

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42 processes an image signal received from the system board 200 connected thereto via the first connector 150. In accordance with this signal processing, the timing controller 42 outputs voltage control signals for controlling the column drivers 22 and row drivers 32, respectively.
